

CPCI Hybrid Bridge Board (HYB)

CPCIe-to-CPCI bridge board bridges between CPCIe slots and CPCI slots installs in the hybrid bridge slot; of an OSS-hybrid backplane.

PN: OSS-CPCIe-3U-IOe-HYB

Features

- Bridges between CPCIe and CPCI slots
- Requires hybrid slot on backplane
- Available in 3U and 6U



Specifications

Form Factor:	3U or 6U Compact PCI Express, PICMG EXP.0, Single Slot
Slot Type:	PICMG EXP.0 bussed hybrid slot
Board Dimensions:	3.937" H x 6.299" L x .62in W (100 x 160 x 15.7 mm)
Bridge:	PLX PEX 8114
Backplane Interfaces:	One PCIe x4 link on Advanced Differential Fabric (ADF) connector XJ3 One 32-Bit/33 or 66 MHz PCI bus on HM connector J1 & power/additional PCI signals
Front Panel Indicators:	Bank of 4 PCIe lane active indicators (green)
CPCI Interfaces:	32-bit at 66 or 33 MHz 3.3V I/O Internal arbiter supports up to 4 external masters 4 CPCI slots (up to 4 PCI clocks/bus requests/bus grants)
Power Consumption:	3.3V @ 2A
Temperature Range:	0° to 55°C (32° to 122°F)
Relative Humidity:	5 to 90% non-condensing
Shock:	30g acceleration peak (11ms pulse)
Vibration:	5-17 Hz 0.5" double amplitude displacement; 7-2000Hz, 1.5g acceleration
Agency Compliance:	FCC Class B CE RoHS